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PTO/SB/21 (01-03)  
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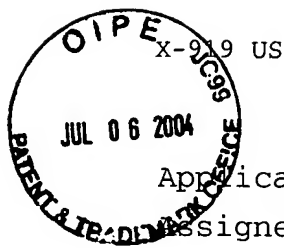
<b>TRANSMITTAL FORM</b> <small>(to be used for all correspondence after initial filing)</small>	Application / Conf. No.	10/001,871 / 3401
	Filing Date	November 19, 2001
	First Named Inventor	Stephen M. Douglass
	Examiner Name	Kenneth S. Kim
	Art Unit	2111
	Patent No.	
Mail Stop:		
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Total Number of Pages in This Submission	Attorney Docket Number	X-919 US

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (with Recordation Cover Sheet)	<input type="checkbox"/> After Allowance Communication to
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Firm or Customer Number	24309 (Customer Number)	Reg. Number 51,959
Attn: Justin Liu		
Signature		
Date	July 1, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Stephen M. Douglass  
Assignee: Xilinx, Inc.  
Title: "Method and Apparatus for Processing Data Within  
A Programmable Gate Array Using Fixed and  
Programmable Processors"

Serial No.: 10/001,871 Filed: 11/19/2001  
Examiner: Kenneth S. Kim Art Unit: 2111  
Docket No.: X-919 US Conf. No.: 3401

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COMMISSIONER FOR PATENTS  
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RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement mailed from the Patent Office on June 2, 2004, Applicants provisionally elect to prosecute Group I: Claims 1-10 and 23-31 with traverse. Applicants hereby cancel Group II, Claims 11-17 and 32-38, and Group III, Claims 18-22 and 39-43, but reserve the right to prosecute Group II and Group III in subsequent divisional applications.

Respectfully submitted,

Justin Liu  
Attorney for Applicants  
Reg. No. 51,959

*I hereby certify that this correspondence is being deposited with the United States Postal Service as **first class mail** in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on July 1, 2004.*

Julie Matthews  
Name

  
Signature